

S/N 08/902133



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.
Serial No.: 08/902,133
Filed: July 29, 1997
Title: MEMORY DEVICE (as amended)

Examiner: George Eckert II
Group Art Unit: 2815
Docket: 303.356US1

INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No :08/902133

Filing Date: July 29, 1997

Title: DYNAMIC ELECTRICALLY ALTERABLE PROGRAMMABLE READ ONLY MEMORY DEVICE

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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES ET AL.

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Date 10 November 2003 By Robert E. Mates
Robert E. Mates
Reg. No. 35,271

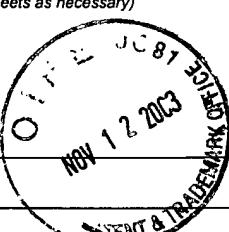
CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 10th day of November, 2003.

Name

Amy Moriarty

Signature

Amy Moriarty

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|---|--|--------------------------------|-------------------|--|--|--|
| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> | | Complete if Known | | | | |
|  | | Application Number | 08/902,133 | | | |
| | | Filing Date | July 29, 1997 | | | |
| | | First Named Inventor | Forbes, Leonard | | | |
| | | Group Art Unit | 2815 | | | |
| | | Examiner Name | Eckert II, George | | | |
| Sheet 1 of 1 | | Attorney Docket No: 303.356US1 | | | | |

US PATENT DOCUMENTS

| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | Filing Date If Appropriate |
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
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EXAMINER

DATE CONSIDERED

387
S/N 08/902133

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Title: MEMORY DEVICE (as amended)

COMMUNICATION CONCERNING RELATED APPLICATION(S)

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Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

| <u>Serial/Patent No.</u> | <u>Filing Date</u> | <u>Attorney Docket</u> | <u>Title</u> |
|--------------------------|--------------------|------------------------|--|
| 08/903452 | July 29, 1997 | 303.324US1 | TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE |
| 09/256643 | February 23, 1999 | 303.324US2 | TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE |
| 09/652420 | August 31, 2000 | 303.324US3 | TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE |
| 09/691004 | October 18, 2000 | 303.324US4 | TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE |
| 08/903486 | July 29, 1997 | 303.326US1 | SILICON CARBIDE GATE TRANSISTOR |

COMMUNICATION CONCERNING CO-PENDING APPLICATIONS

Serial Number: 08/902133

Filing Date: July 29, 1997

Title: DYNAMIC ELECTRICALLY ALTERABLE PROGRAMMABLE READ ONLY MEMORY DEVICE

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| 08/902843 | July 29, 1997 | 303.354US1 | DEAPROM HAVING AMORPHOUS SILICON CARBIDE GATE INSULATOR |
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COMMUNICATION CONCERNING CO-PENDING APPLICATIONS

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| 09/650553 | August 30, 2000 | 303.378US3 | CARBURIZED SILICON GATE INSULATORS FOR INTEGRATED CIRCUITS |
| 10/461593 | June 11, 2003 | 303.356US3 | DYNAMIC ELECTRICALLY ALTERABLE PROGRAMMABLE READ ONLY MEMORY DEVICE |

Respectfully submitted,

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By Applicants' Representatives,

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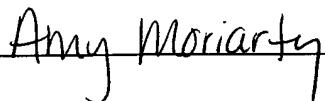
Date 10 November 2003 By _____

Robert E. Mates
Reg. No. 35,271



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